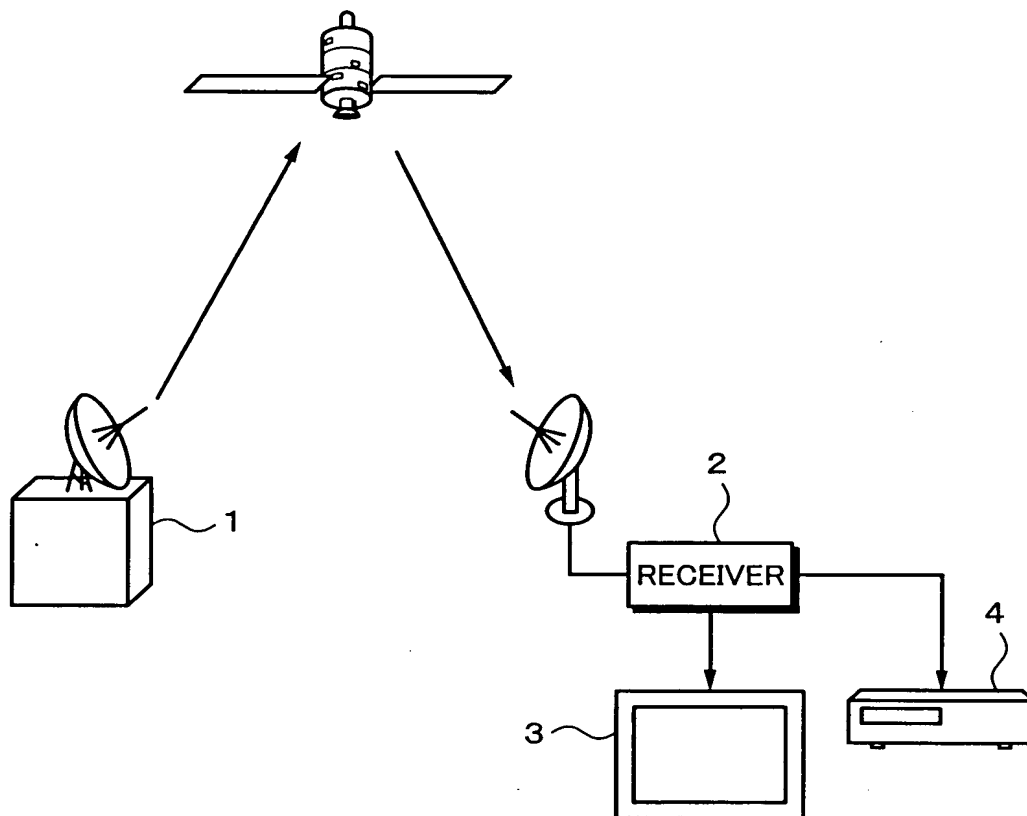


Fig. 1

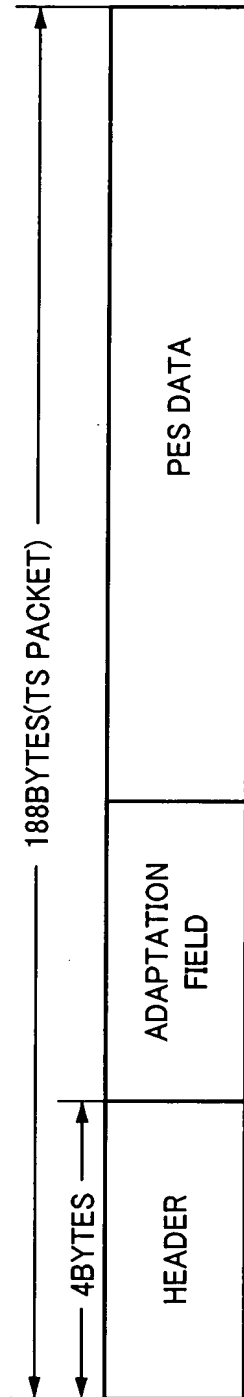


Fig. 2A

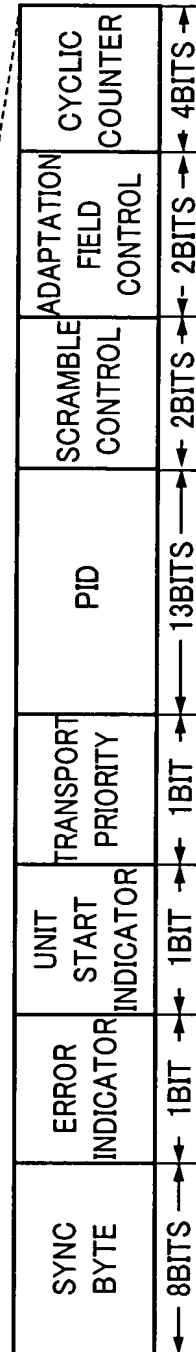


Fig. 2B

Fig. 3A

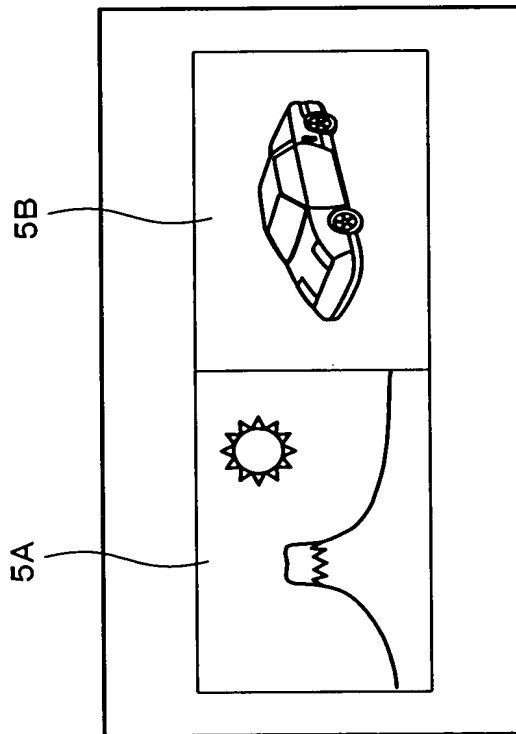


Fig. 3B

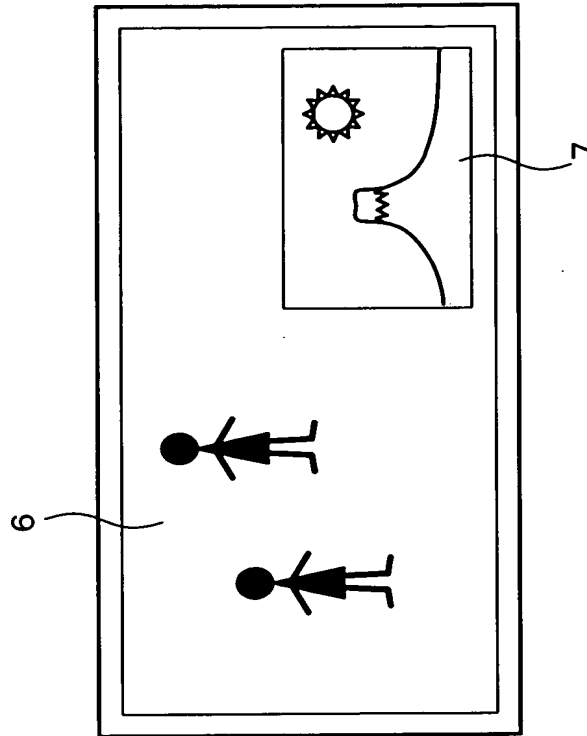


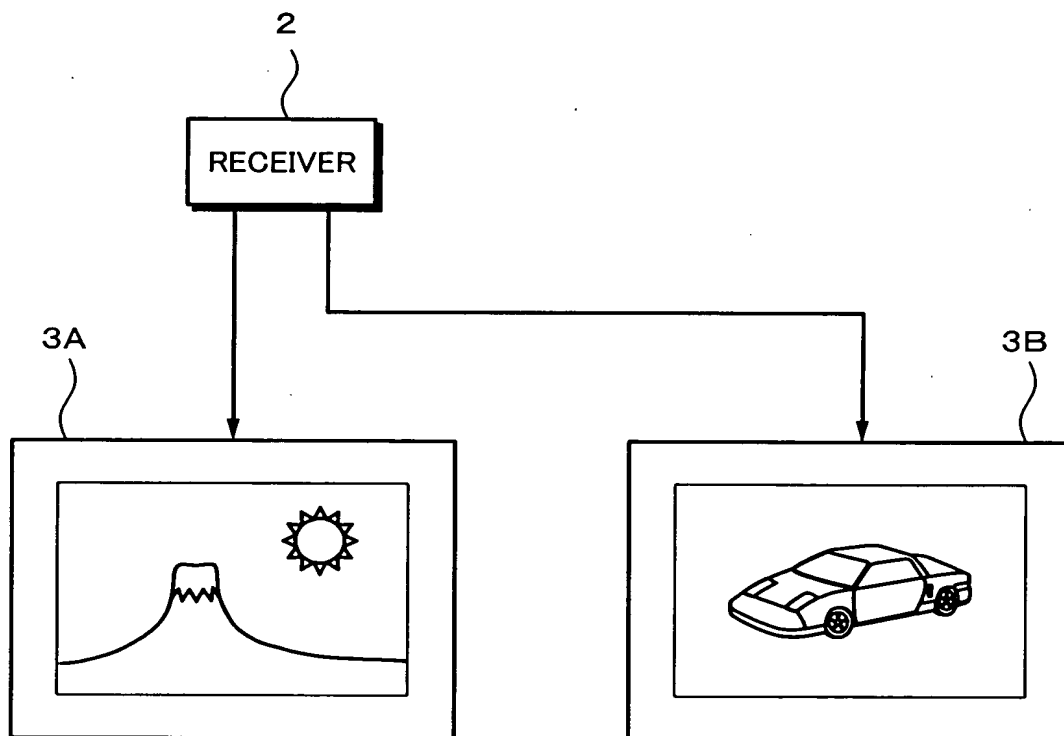
Fig. 4

Fig. 5

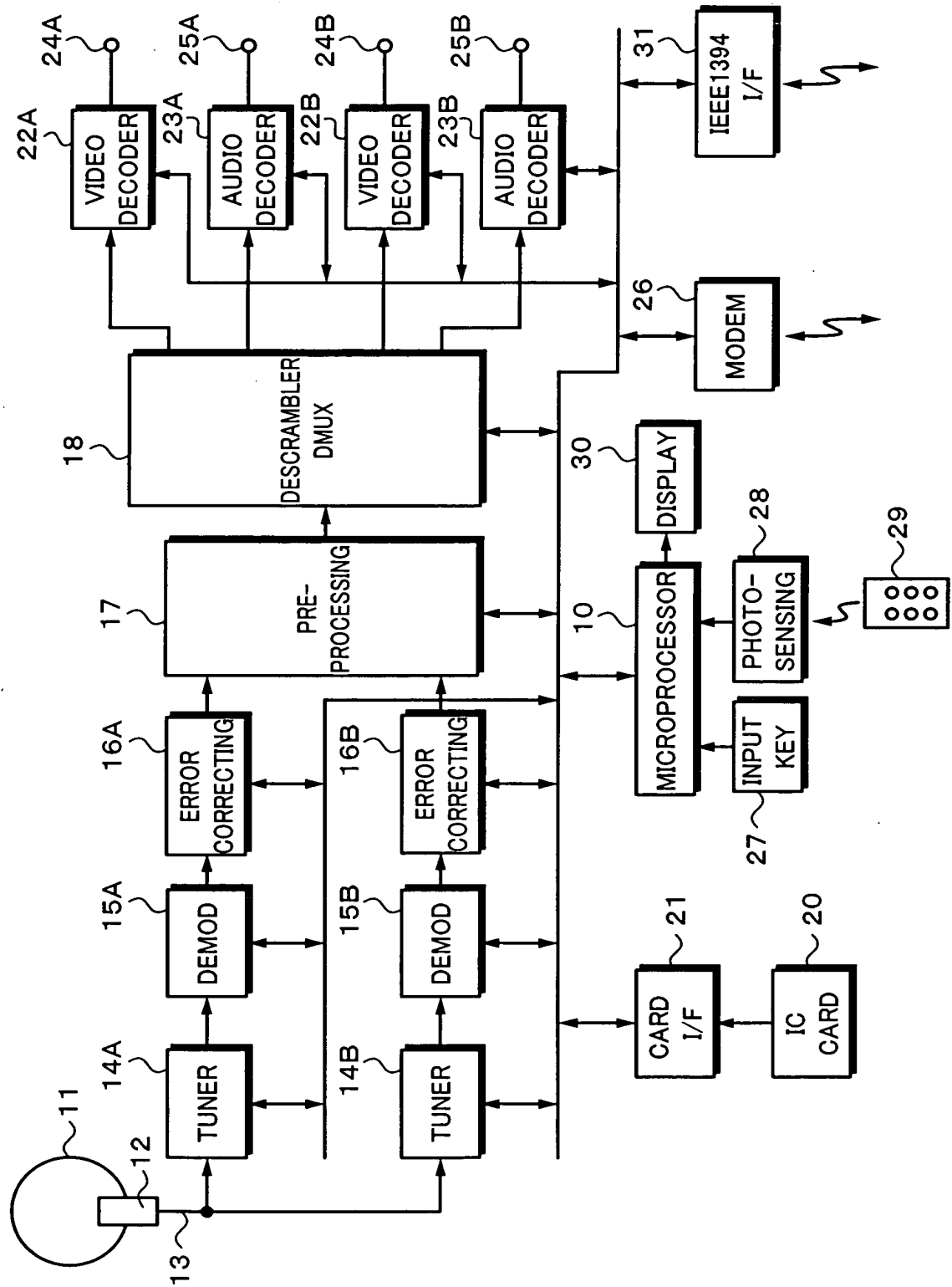


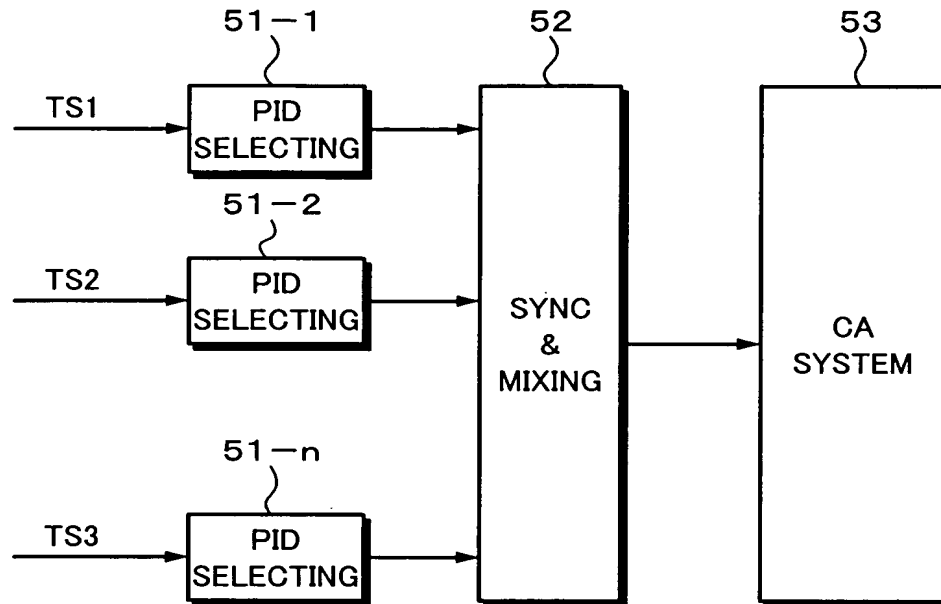
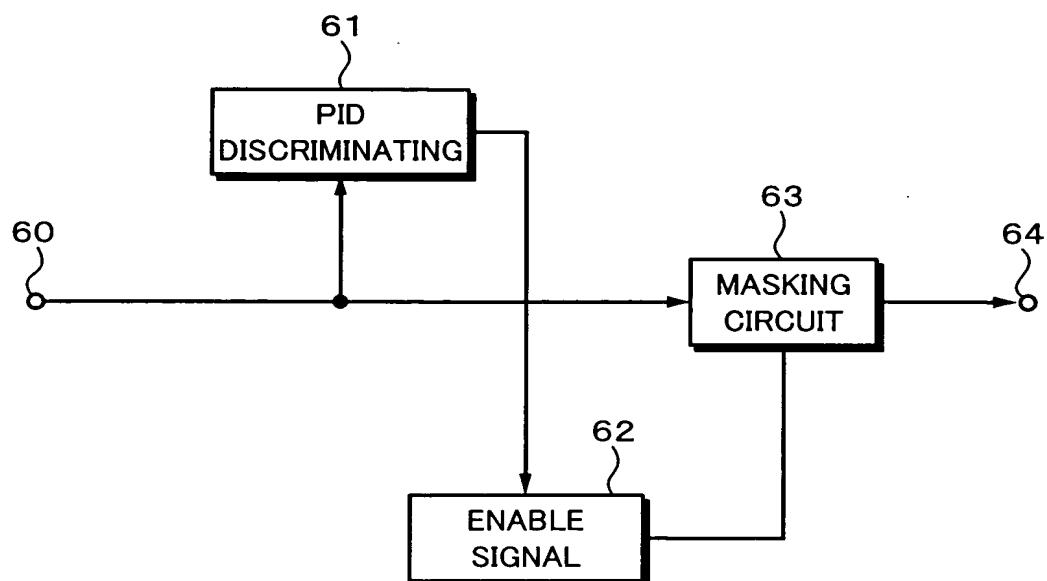
Fig. 6**Fig. 7**

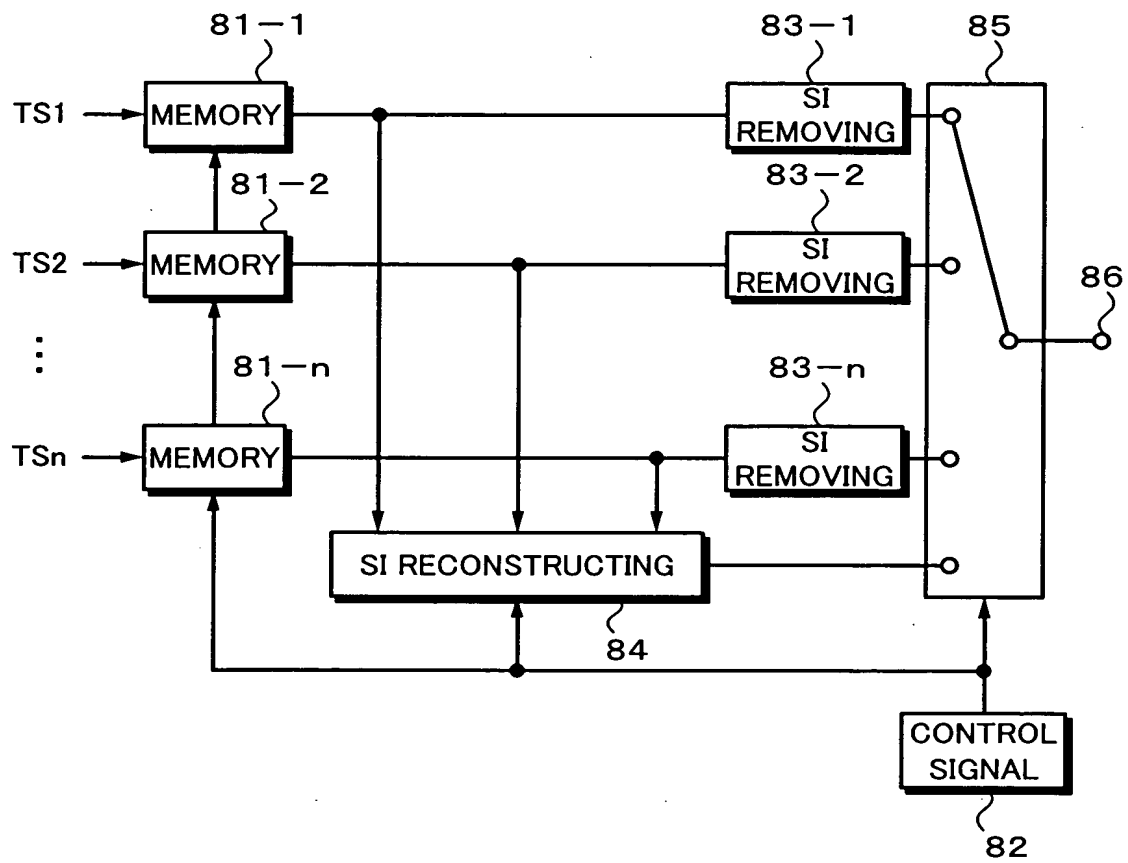
Fig. 8

Fig. 9

	PAT	
91	Version_number current_next_indicator section_number last_section_number	HEADER INFORMATION OF PAT
92	PROGRAM1 PROGRAM NUMBER(program_number) PID OF PMT	INFORMATION OF EACH PROGRAM
	PROGRAM2 PROGRAM NUMBER(program_number) PID OF PMT	
	
93	CRC_32	ERROR DETECTION

Fig. 10A

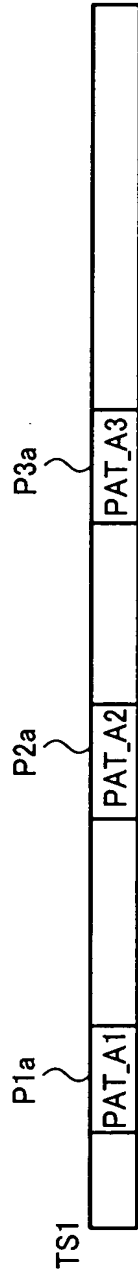


Fig. 10B

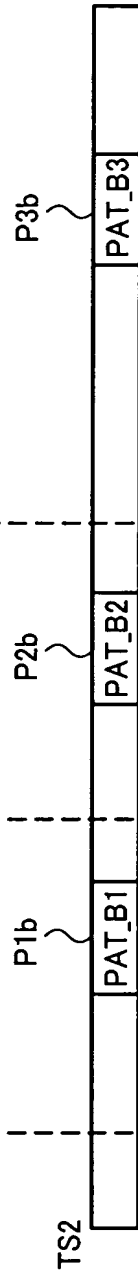


Fig. 10C

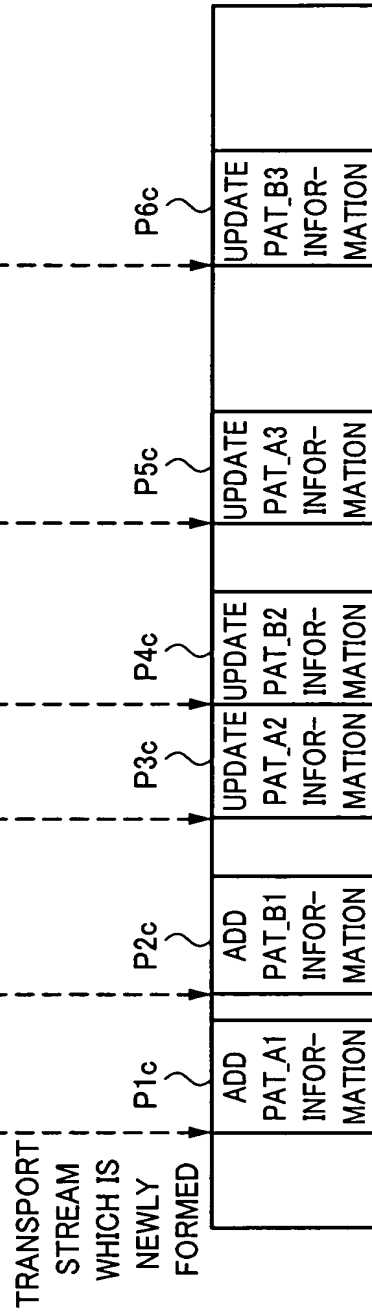


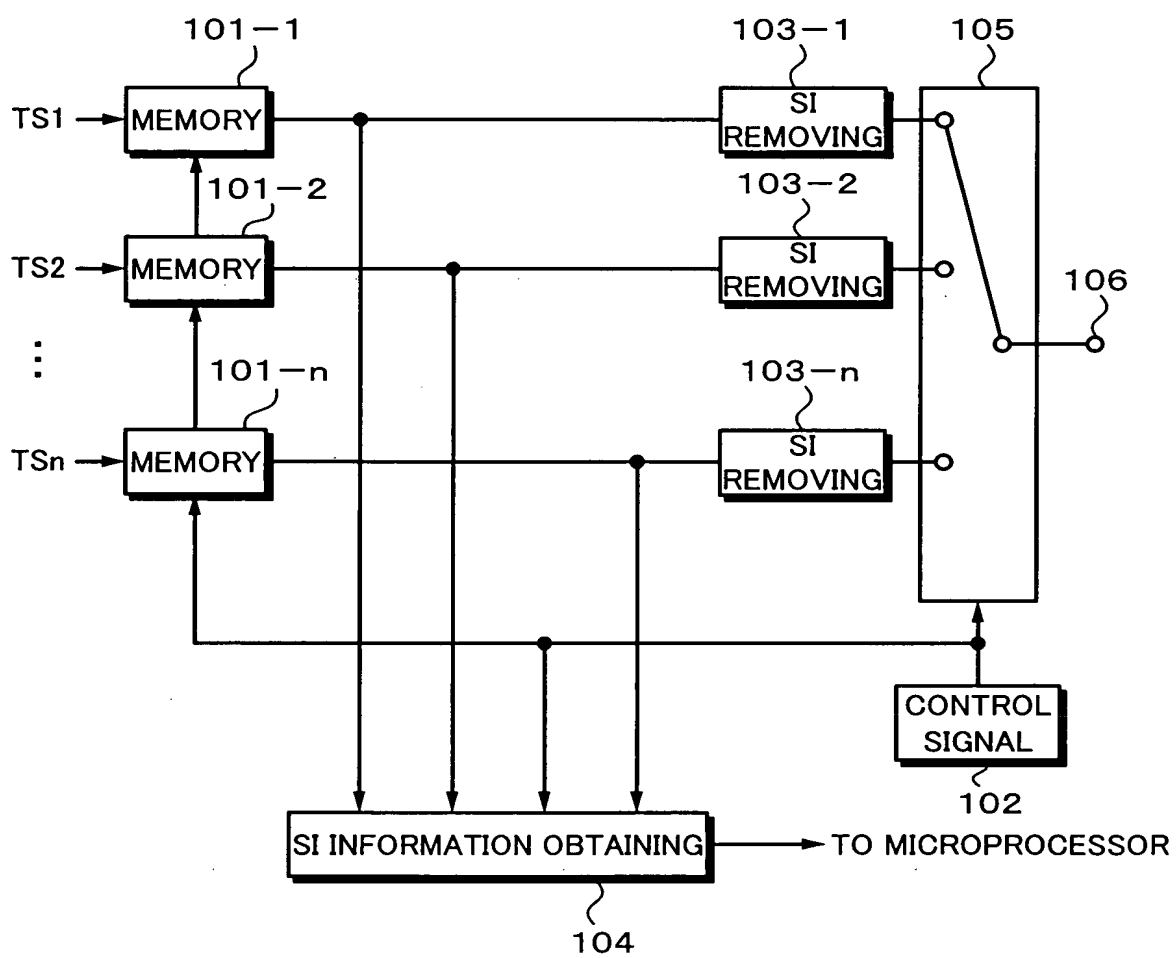
Fig. 11

Fig. 12

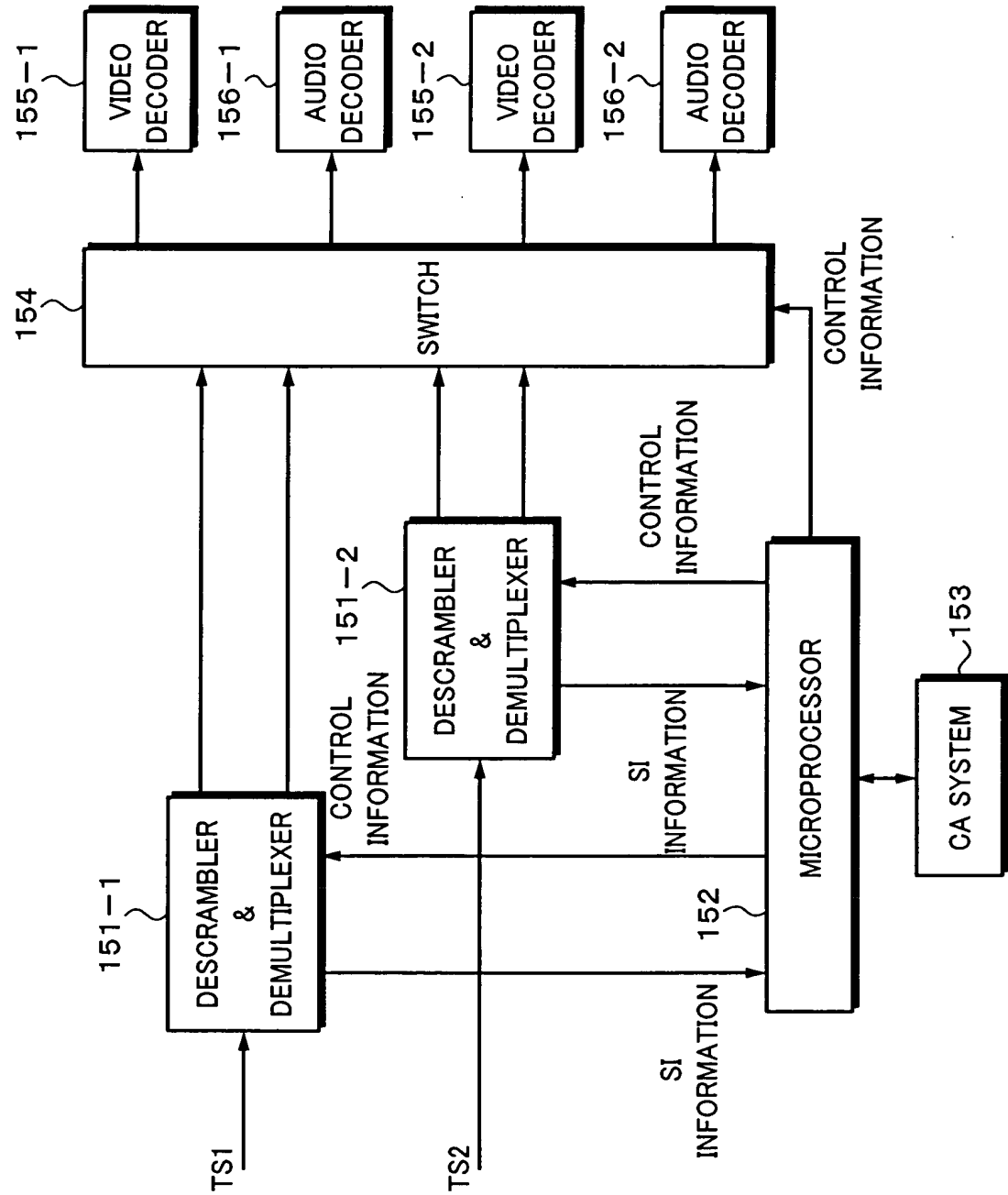
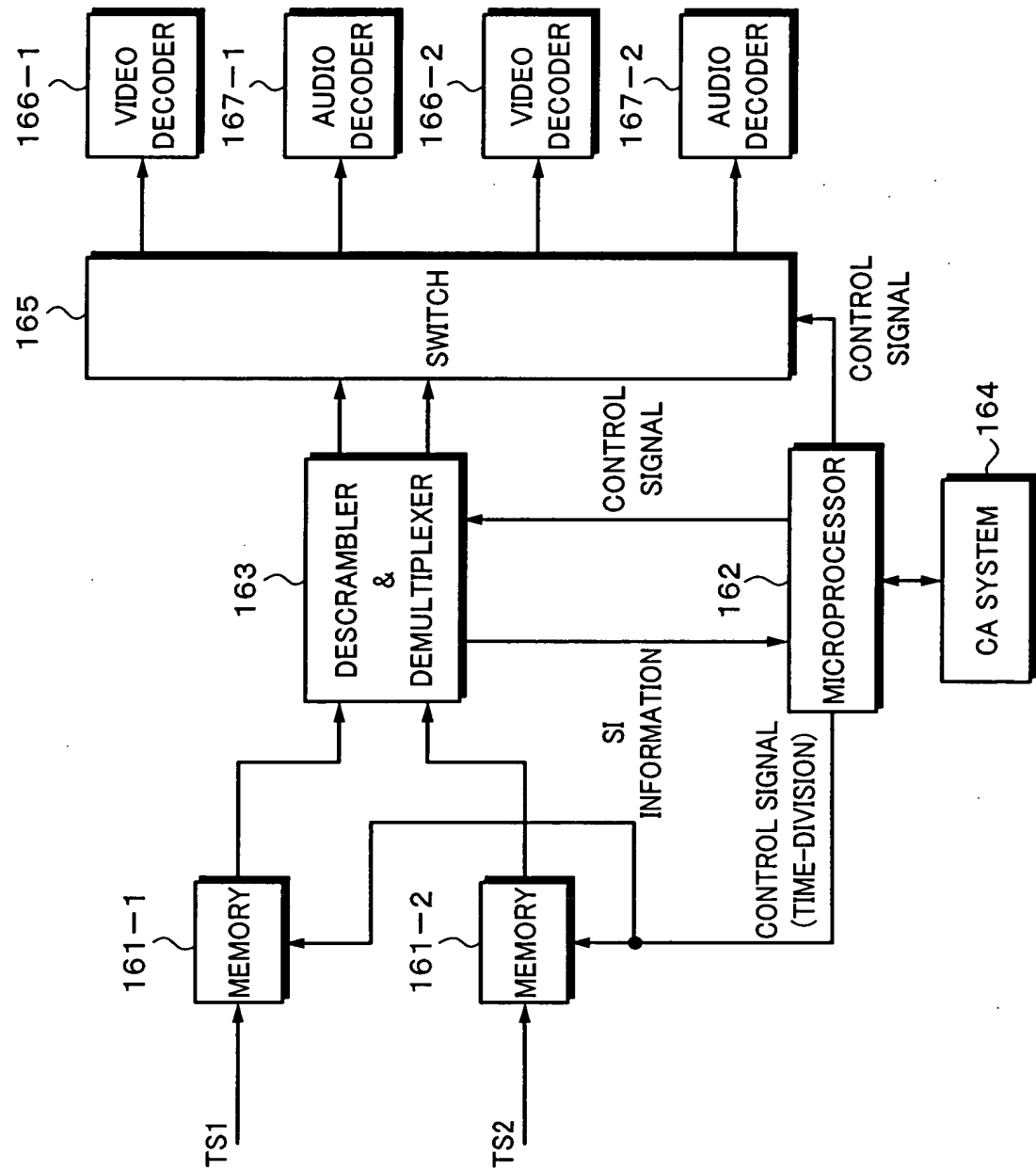


Fig. 13



DESCRIPTION OF REFERENCE NUMERALS

- 17 PREPROCESSING CIRCUIT
- 51-1, 51-2, ..., 51-n SELECTING CIRCUIT
- 52 SYNC & MIXING CIRCUIT
- 53 CA SYSTEM
- 61 PID DISCRIMINATING CIRCUIT
- 62 ENABLE SIGNAL SETTING CIRCUIT
- 63 MASKING CIRCUIT